

A-3 Auxiliary Area

A000 to A447: Read-only Area, A448 to A1000: Read/Write Area

A-3-1 Read-only Area (Set by System)

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A0	---	10-ms Incrementing Free Running Timer	<p>This word contains the system timer used after the power is turned ON.</p> <p>A0 is set to 0000 hex when the power is turned ON and this value is automatically incremented by 1 every 10 ms. The value returns to 0000 hex after reaching FFFF hex (655,350 ms), and then continues to be automatically incremented by 1 every 10 ms.</p> <p>The timer will continue to be incremented when the operating mode is switched to RUN mode.</p> <p>Example: The interval can be counted between processing A and processing B without requiring timer instructions. This is achieved by calculating the difference between the value in A000 for processing A and the value in A000 for processing B. The interval is counted in 10 ms units.</p>	Retained	Cleared	Every 10 ms after power is turned ON
A1	---	100-ms Incrementing Free Running Timer	<p>This word contains the system timer used after the power is turned ON.</p> <p>A1 is set to 0000 hex when the power is turned ON and this value is automatically incremented by 1 every 100 ms. The value returns to 0000 hex after reaching FFFF hex (6,553,500 ms), and then continues to be automatically incremented by 1 every 100 ms.</p> <p>The timer will continue to be incremented when the operating mode is switched to RUN mode.</p>	Retained	Cleared	Every 100 ms after power is turned ON
A2	---	1-s Incrementing Free Running Timer	<p>This word contains a system timer used after the power is turned ON.</p> <p>A2 is set to 0000 hex when the power is turned ON and this value is automatically incremented by 1 every 1 s. The value returns to 0000 hex after reaching FFFF hex (65,535 s), and then continues to be automatically incremented by 1 every 1 s.</p> <p>The timer will continue to be incremented when the operating mode is switched to RUN mode.</p>	Retained	Cleared	Every 1 s after power is turned ON
A50 to A69	A50.00 to A50.07	Basic I/O Unit Information, Rack 0 Slot 0	<p>A bit will turn ON to indicate when the load short-circuit protection function alarm output has been given.</p> <p>Only the 4 most LSB are used for the CJ1W-OD202 (2 points per bit), only the LSB is used for the CJ1W-OD212, OD204, MD232 and only the two most LSB are used for the CJ1W-OD232.</p> <p>ON: Short circuited</p> <p>OFF: Normal</p>	---	---	Refreshed each cycle.
	A50.08 to A50.15	Basic I/O Unit Information, Rack 0 Slot 1		---	---	
	A51.00 to A69.15	Basic I/O Unit Information, Racks 0 Slot 2 to Rack 3 Slot 9		---	---	
A90 to A93	---	User Program Date	<p>These words contain in BCD the date and time that the user program was last overwritten.</p> <p>A90.00 to A90.07: Seconds (00 to 59)</p> <p>A90.08 to A90.15: Minutes (00 to 59)</p> <p>A91.00 to A91.07: Hour (00 to 23)</p> <p>A91.08 to A91.15: Day of month (01 to 31)</p> <p>A92.00 to A92.07: Month (01 to 12)</p> <p>A92.08 to A92.15: Year (00 to 99)</p> <p>A93.08 to A93.07: Day of the week</p> <p>(00: Sunday, 01: Monday, 02: Tuesday, 03: Wednesday, 04: Thursday, 05: Friday, 06: Saturday)</p>	Retained	Retained	---
A94 to A97	---	Parameter Date	<p>These words contain in BCD the date and time that the parameters were last overwritten.</p> <p>The format is the same as above</p>	Retained	Retained	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A99	00	UM Read Protection Status	Indicates whether the entire user program in the PLC is read-protected. OFF: UM not read-protected. ON: UM read-protected.	Retained	Retained	When protection is set or cleared
	01	Task Read Protection Status	Indicates whether read protection is set for individual tasks. OFF: Tasks not read-protected. ON: Tasks read-protected.			---
	02	Program Write Protection Status when Read Protection Is Set	Indicates whether the program is write-protected. OFF: Write-enabled. ON: Write-protected.			---
	03	Enable/Disable Status for Backing Up the Program to a Memory Card	Indicates whether creating a backup program file (.OBJ) is enabled or disabled. OFF: Enabled. ON: Disabled.			---
	14	IR/DR Operation between Tasks	Turn ON this bit to share index and data registers between all tasks. Turn OFF this bit to use separate index and data registers between in each task. OFF: Independent ON: Shared (default)			---
A100 to A199	---	Error Log Area	When an error has occurred, the error code, error contents, and error's time and date are stored in the Error Log Area. Information on the 20 most recent errors can be stored. Each error record occupies 5 words; the function of these 5 words is as follows: First Word: Error code (bits 0 to 15) Second Word: Error contents (bits 0 to 15) Error contents: Address of Auxiliary Area word with details or 0000. Third Word: Minutes (bits 8 to 15), Seconds (bits 0 to 7) Seconds: 00 to 59, BCD Minutes: 00 to 59, BCD Fourth Word: Day of month (bits 8 to 15), Hours (bits 0 to 7) Hours: 00 to 23, BCD Day of month: 01 to 31, BCD Fifth Word: Year (bits 8 to 15), Month (bits 0 to 7) Year: 00 to 99, BCD Month: 00 to 12, BCD Errors generated by FAL(006) and FALS(007) will also be stored in this Error Log. The Error Log Area can be reset from the CX-Programmer. If the Error Log Area is full (20 records) and another error occurs, the oldest record in A100 to A104 will be cleared, the other 19 records will be shifted down, and the new record will be stored in A195 to A199.	Retained	Retained	Refreshed when error occurs. A50014 A300 A400

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A200	A200.11	First Cycle Flag	ON for one cycle after PLC operation begins (after the mode is switched from PROGRAM to RUN or MONITOR, for example). ON for the first cycle	---	---	---
	A200.12	Step Flag	ON for one cycle when step execution is started with STEP(008). This flag can be used for initialization processing at the beginning of a step. ON for the first cycle after execution of STEP(008).	Cleared	---	---
	A200.14	Task Started Flag	When a task switches from STANDBY or DISABLED to READY status, this flag will be turned ON within the task for one cycle only. ON: ON for first cycle (including transitions from STANDBY and DISABLED status) OFF: Other The only difference between this flag and A200.15 is that this flag also turns ON when the task switches from STANDBY to READY status.	Cleared	Cleared	---
	A200.15	First Task Start Flag	ON when a task is executed for the first time. This flag can be used to check whether the current task is being executed for the first time so that initialization processing can be performed if necessary. ON: First execution OFF: Not executable for the first time or not being executed.	Cleared	---	---
A201	A201.10	Online Editing Wait Flag	ON when an online editing process is waiting. ON: Waiting for online editing OFF: Not waiting for online editing If another online editing command is received while waiting, the other command will not be recorded and an error will occur.	Cleared	Cleared	A527
	A201.11	Online Editing Flag	ON when an online editing process is being executed. ON: Online editing in progress OFF: Online editing not in progress	Cleared	Cleared	A527
A202	A202.00 to A202.07	Communications Port Enabled Flags	ON when a network instruction (SEND, RECV, CMND, PMCR, TXDU, or RXDU) or background execution can be executed with the corresponding port number. Bits 00 to 07 correspond to communications ports 0 to 7. ON: Network instruction is not being executed OFF: Network instruction is being executed (port busy) When two or more network instructions are programmed with the same port number, use the corresponding flag as an execution condition to prevent the instructions from being executed simultaneously. (The flag for a given port is turned OFF while a network instruction with that port number is being executed.) Cleared when an instruction is executed.	Cleared	---	---
	A202.08	CJ2 Instructions Enabled Flag	ON when CJ2 instructions can be used. This flag is ON by default. This flag can be used only with the following instructions: SEND2, CMND2, PMCR2, and RECV2.	---	Updated according to internal status (cleared).	---
	A202.15	Network Communications Port Allocation Enabled Flag	ON when there is a communications port available for automatic allocation when executing communications instructions (SEND, RECV, CMND, PMCR, TXDU, or RXDU). ON: Communications port available OFF: Communications port not available Use this flag to confirm whether a communications port is available for automatic allocation before executing communications instructions when using 9 or more communications instructions simultaneously.	Cleared	---	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A203 to A210	---	Communications Port Completion Codes	<p>These words contain the completion codes for the corresponding port numbers when network instructions (SEND, RECV, CMND, or PMCR) or background execution have been executed.</p> <p>(The corresponding word will be cleared to 0000 hex when background execution has been completed.)</p> <p>Words A203 to A210 correspond to communications ports 0 to 7.</p> <p>Non-zero: Error code 0000: Normal condition</p> <p>The following codes will be stored when an Explicit Message Instruction (EXPLT, EGATR, ESATR, ECHRD, or ECHWR) has been executed.</p> <p>If the Explicit Communications Error Flag turns OFF, 0000 hex is stored.</p> <p>If the Explicit Communications Error Flag is ON and the Network Communications Error Flag is ON, the FINS end code is stored.</p> <p>If the Explicit Communications Error Flag is ON and the Network Communications Error Flag is OFF, the explicit message end code is stored.</p> <p>During communications, 0000 hex will be stored and the suitable code will be stored when execution has been completed. The code will be cleared when operation is started.</p> <p>(The completion code for a given port is cleared to 0000 when a network instruction with that port number is executed.)</p> <p>Cleared when an instruction is executed.</p>	Cleared	---	---
A211	---	Number of Ports Available for CJ2 Network Communications Instruction	<p>When the number of CJ2 network communications instructions that can be used reaches 0, A202.08 will turn OFF. This word can be used only with the following instructions: SEND2, CMND2, PMCR2, and RECV2.</p> <p>The contents of this word can be used to check communications traffic.</p>	---	Updated according to internal status (cleared).	---
A213	A213.00 to A213.07	Explicit Communications Error Flag	<p>Turn ON when an error occurs in executing an Explicit Message Instruction (EXPLT, EGATR, ESATR, ECHRD, or ECHWR).</p> <p>Bits 00 to 07 correspond to communications ports 0 to 7.</p> <p>ON: Error end OFF: Normal end</p> <p>The corresponding bit will turn ON both when the explicit message cannot be sent and when an error response is returned for the explicit message.</p> <p>The status will be maintained until the next explicit message communication is executed. The bit will always turn OFF when the next Explicit Message Instruction is executed.</p>	Cleared	---	A219.00 to A219.07 A203 to A210
A214	A214.00 to A214.07	First Cycle Flags after Network Communications Finished	<p>Each flag will turn ON for just one cycle after communications have been completed. Bits 00 to 07 correspond to ports 0 to 7. Use the Used Communications Port Number stored in A218 to determine which flag to access.</p> <p>1: First cycle after communications finish only 2: Other status</p> <p>These flags are not effective until the next cycle after the communications instruction is executed. Delay accessing them for at least one cycle.</p> <p>Use the port number specified in A218 (Used Communications Port Numbers) to access the correct bit for the port being used.</p>	Retained	Cleared	---
A215	A215.00 to A215.07	First Cycle Flags after Network Communications Error	<p>Each flag will turn ON for just one cycle after a communications error occurs. Bits 00 to 07 correspond to ports 0 to 7. Use the Used Communications Port Number stored in A218 to determine which flag to access. Determine the cause of the error according to the Communications Port Completion Codes stored in A203 to A210.</p> <p>ON: First cycle after communications error only OFF: Other status</p> <p>These flags are not effective until the next cycle after the communications instruction is executed. Delay accessing them for at least one cycle.</p> <p>Use the port number specified in A218 (Used Communications Port Numbers) to access the correct bit for the port being used.</p>	Retained	Cleared	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A216 and A217	---	Network Communications Completion Code Storage Address	The completion code for a communications instruction is automatically stored at the address with the I/O memory address given in these words. Place this address into an index register and use indirect addressing through the index register to read the communications completion code.	Retained	Cleared	---
A218	---	Used Communications Port Numbers	Stores the communications port numbers used when a communications instruction is executed using automatic communication port allocations. 0000 to 0007 hex: Communications port 0 to 7	Retained	Cleared	---
A219	A219.00 to A219.07	Communications Port Error Flags	ON when an error occurred during execution of a network instruction (SEND, RECV, CMND, or PMCR). Bits 00 to 07 correspond to communications ports 0 to 7. ON: Error occurred OFF: Normal condition	Retained	---	---
A220 to A259	A220.00 to 259.15	Basic I/O Unit Input Response Times	These words contain the actual input response times. 0 to 17 hexadecimal When the Basic I/O Unit input response time setting is changed in the PLC Setup while the PLC is in PROGRAM mode, the setting in the PLC Setup will not match the actual value in the Basic I/O Unit unless the power is turned OFF and then ON again. In that case, the actual value can be monitored in these words.	Retained	See function column.	PLC Setup (Basic I/O Unit Input response time settings)
A260	---	I/O Allocation Status	Indicates the current status of I/O allocation, i.e., Automatic I/O Allocation or User-set I/O Allocations. 0000 hex: Automatic I/O allocations BBBB hex: User-set I/O allocations	Retained	Retained	---
A261	A261.00	I/O Table Creation Error Details	ON: Error in CPU Bus Unit Setup Turns OFF when I/O tables are generated normally. ON: Error in CPU Bus Unit Setup OFF: I/O tables generated normally	Retained	Cleared	When I/O tables are generated
	A261.02		ON: Overflow in maximum number of I/O points Turns OFF when I/O tables are generated normally. ON: Overflow in maximum number of I/O points OFF: I/O tables generated normally			A401.11 (Too many I/O points)
	A261.03		ON: The same unit number was used more than once. Turns OFF when I/O tables are generated normally. ON: The same unit number was used more than once. OFF: I/O tables generated normally			A401.13 (duplicated number)
	A261.04		ON: I/O bus error Turns OFF when I/O tables are generated normally. ON: I/O bus error OFF: I/O tables generated normally			A401.14 (I/O bus error)
	A261.06		ON: I/O table error because a SYSMAC BUS Slave cannot be detected Turns OFF when I/O tables are generated normally. ON: SYSMAC BUS Slave missing OFF: I/O tables generated normally			---
	A261.07		ON: Error in a Special I/O Unit Turns OFF when I/O tables are generated normally. ON: Error in a Special I/O Unit OFF: I/O tables generated normally			---
	A261.09		ON: I/O detection has not been completed. Turns OFF when I/O tables are generated normally. ON: I/O detection has not been completed. OFF: I/O tables generated normally			---
A262 and A263	---	Maximum Cycle Time (0.1-ms increments)	These words contain the maximum cycle time since the start of PLC operation. The cycle time is recorded in 8-digit hexadecimal with the leftmost 4 digits in A263 and the rightmost 4 digits in A262. 0 to FFFFFFFF: 0 to 429,496,729.5 ms (0.1-ms increments)	Cleared	Cleared	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A264 and A265	---	Present Cycle Time (0.1-ms increments)	These words contain the present cycle time in increments of 0.1 ms. The time is recorded each cycle in 8-digit hexadecimal with the leftmost 4 digits in A264 and the rightmost 4 digits in A265. 00000000 to FFFFFFFF (0.0 to 429,496,729.5 ms)	Cleared	Cleared	---
A266 and A267	---	Present Cycle Time (0.01-ms increments)	These words contain the present cycle time in increments of 0.01 ms. The time is recorded each cycle in 8-digit hexadecimal with the leftmost 4 digits in A266 and the rightmost 4 digits in A267. 00000000 to FFFFFFFF (0.0 to 42,949,672.95 ms)	Cleared	Cleared	---
A270 and A271	---	High-speed Counter 0 PV	Contains the PV of high-speed counter 0. Lower 4 digits: A270, Upper 4 digits: A271	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status. Refreshed when PRV2(883) instruction is executed to convert high-speed counter PV to total number of pulses. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value.
A272 and A273	---	High-speed Counter 1 PV	Contains the PV of high-speed counter 1. Lower 4 digits: A272, Upper 4 digits: A273	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status. Refreshed when PRV2(883) instruction is executed to convert high-speed counter PV to total number of pulses. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A274	A274.00	High-speed Counter 0 Range Comparison Condition 1 In-range Flag	These flags indicate whether the PV is within any of the eight ranges when high-speed counter 0 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left. OFF: Not in range, ON: In range	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Refreshed when reset.
	A274.01	High-speed Counter 0 Range Comparison Condition 2 In-range Flag				
	A274.02	High-speed Counter 0 Range Comparison Condition 3 In-range Flag				
	A274.03	High-speed Counter 0 Range Comparison Condition 4 In-range Flag				
	A274.04	High-speed Counter 0 Range Comparison Condition 5 In-range Flag				
	A274.05	High-speed Counter 0 Range Comparison Condition 6 In-range Flag				
	A274.06	High-speed Counter 0 Range Comparison Condition 7 In-range Flag				
	A274.07	High-speed Counter 0 Range Comparison Condition 8 In-range Flag				
	A274.08	High-speed Counter 0 Comparison Operation	This flag indicates whether a comparison operation is being executed for high-speed counter 0. OFF: Stopped, ON: Comparing	Cleared	Cleared	Refreshed when comparison operation starts or stops.
	A274.09	High-speed Counter 0 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of high-speed counter 0. (Used with the linear mode counting range only.) OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the PV is changed. Refreshed when an overflow or underflow occurs.
	A274.10	High-speed Counter 0 Count Direction	This flag indicates whether the high-speed counter is currently being incremented or decremented. The counter PV for the current cycle is compared with the PV in last cycle to determine the direction. OFF: Decrementing, ON: Incrementing	---	---	<ul style="list-style-type: none"> Setting used for high-speed counter, valid during counter operation. Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A275	A275.00	High-speed Counter 1 Range Comparison Condition 1 In-range Flag	These flags indicate whether the PV is within any of the eight ranges when high-speed counter 1 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left. OFF: Not in range, ON: In range	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Refreshed when reset.
	A275.01	High-speed Counter 1 Range Comparison Condition 2 In-range Flag				
	A275.02	High-speed Counter 1 Range Comparison Condition 3 In-range Flag				
	A275.03	High-speed Counter 1 Range Comparison Condition 4 In-range Flag				
	A275.04	High-speed Counter 1 Range Comparison Condition 5 In-range Flag				
	A275.05	High-speed Counter 1 Range Comparison Condition 6 In-range Flag				
	A275.06	High-speed Counter 1 Range Comparison Condition 7 In-range Flag				
	A275.07	High-speed Counter 1 Range Comparison Condition 8 In-range Flag				
	A275.08	High-speed Counter 1 Comparison In-progress Flag	This flag indicates whether a comparison operation is being executed for high-speed counter 1. OFF: Stopped, ON: Comparing	Cleared	Cleared	Refreshed when comparison operation starts or stops.
	A275.09	High-speed Counter 1 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of high-speed counter 1. (Used with the linear mode counting range only.) OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the PV is changed. Refreshed when an overflow or underflow occurs.
	A275.10	High-speed Counter 1 Count Direction	This flag indicates whether high-speed counter 1 is currently being incremented or decremented. The counter PV for the current cycle is compared with the PV in last cycle to determine the direction. OFF: Decrementing, ON: Incrementing	---	---	<ul style="list-style-type: none"> Setting used for high-speed counter, valid during counter operation. Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A276 and A277	---	Pulse Output 0 PV	Contain the number of pulses output from the corresponding pulse output port. 8000 0000 to 7FFF FFFF hex (–2,147,483,648 to 2,147,483,647)	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when INI(880) instruction is executed to change the PV. Cleared when Pulse Output Reset Bit is turned ON. Cleared when pulse output is started (when the origin is not defined). Refreshed when PRV(881) instruction is executed to read the PV or status.
A278 and A279	---	Pulse Output 1 PV	When pulses are being output in the CW direction, the PV is incremented by 1 for each pulse. When pulses are being output in the CCW direction, the PV is decremented by 1 for each pulse. PV after overflow: 7FFF FFFF hex PV after underflow: 8000 000 hex Lower 4 digits: A276/A278/A322/A324 Upper 4 digits: A277/A279/A323/A325			
A280	A280.00	Pulse Output 0 Pulse Output Status Flag	This flag will be ON when pulses are being output from pulse output 0 according to an ORG(889), ACC(888), PLS2(887), or IFEED(892) instruction and the output frequency is being changed in steps (accelerating or decelerating). OFF: Constant speed, ON: Accelerating/decelerating	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process.
	A280.01	Pulse Output 0 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of pulse output 0. OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the INI(880) instruction is executed to change the PV. Refreshed when an overflow or underflow occurs.
	A280.02	Pulse Output 0 Number of Pulses Set Flag	ON when the number of output pulses for pulse output 0 has been set with the PULS(886) instruction. OFF: Not set, ON: Set	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed when the PULS(886) instruction is executed. Cleared when pulse output is stopped.
	A280.03	Pulse Output 0 Output Completed Flag	ON when the number of output pulses set with the PULS(886), PLS2(887), or IFEED(892) instruction has been output through pulse output 0. OFF: Output not completed, ON: Output completed	Cleared	Cleared	Refreshed at the start or completion of pulse output in independent mode.
	A280.04	Pulse Output 0 Outputting Pulses Flag	ON when pulses are being output from pulse output 0. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
	A280.05	Pulse Output 0 No-origin Flag	ON when the origin has not been determined for pulse output 0 and goes OFF when the origin has been determined. OFF: Origin established, ON: Origin not established	Turned ON when operation starts.	Turned ON when operation starts.	<ul style="list-style-type: none"> Turned ON when the pulse output is reset. Turned ON when an origin search is started. Turned ON when a limit input is received and clearing is set. Turned ON when an overflow or underflow occurs. Turned OFF when an origin search is completed. Turned OFF when INI(880) instruction is executed to change the PV.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A280	A280.06	Pulse Output 0 At-origin Flag	ON when the PV of pulse output 0 matches the origin (0). OFF: Not stopped at origin, ON: Stopped at origin	Retained	Cleared	<ul style="list-style-type: none"> Turned ON when stopped at the origin. Turned OFF when the origin is left.
	A280.07	Pulse Output 0 Output Stopped Error Flag	ON when an error occurred while outputting pulses in the pulse output 0 origin search function. The Pulse Output 0 Output Stop Error Code will be written to A444. 0: No error, ON: Stop error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when an origin search is started. Refreshed when a fatal pulse output error occurs during an origin search. Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
	A280.08	Pulse Output 0 Interrupt Feeding In-progress Flag	This flag is turned ON when an interrupt input is received after output from pulse output 0 is started with the IFEED(892) instruction. OFF: Interrupt feeding not in progress. ON: Interrupt feeding in progress.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared during overseeing processing after completing interrupt feeding. Turned ON when the interrupt input turns ON after pulse output is started with the IFEED(892) instruction.
	A280.09	Pulse Output 0 Interrupt Feeding Error Flag	This flag will turn ON if an overflow or underflow occurs when an interrupt input is received, or when the specified number of pulses is moved, after output from pulse outputs 0 is started with the IFEED(892) instruction. ON: No error. OFF: Overflow/underflow or specified number of pulses has been moved.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when IFEED(892) instruction processing is started. Turned ON if an overflow or underflow occurs when an interrupt input is received, or if an overflow or underflow occurs while the specified number of pulses is being moved, after operation is started with the IFEED(892) instruction with the origin defined.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A281	A281.00	Pulse Output 1 Pulse Output Status Flag	This flag will be ON when pulses are being output from pulse output 1 according to an ORG(889), ACC(888), PLS2(887), or IFEED(892) instruction and the output frequency is being changed in steps (accelerating or decelerating). OFF: Constant speed, ON: Accelerating/decelerating	Cleared	Cleared	Refreshed each cycle during over-seeing process.
	A281.01	Pulse Output 1 Overflow/Under- flow Flag	This flag indicates when an overflow or underflow has occurred in the PV of pulse output 1. OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared when the INI(880) instruction is executed to change the PV. • Refreshed when an overflow or underflow occurs.
	A281.02	Pulse Output 1 Number of Pulses Set Flag	ON when the number of output pulses for pulse output 1 has been set with the PULS(886) instruction. OFF: Not set, ON: Set	Cleared	Cleared	<ul style="list-style-type: none"> • Refreshed when the PULS(886) instruction is executed. • Cleared when pulse output is stopped.
	A281.03	Pulse Output 1 Output Completed Flag	ON when the number of output pulses set with the PULS(886), PLS2(887), or IFEED(892) instruction has been output through pulse output 1. OFF: Output not completed, ON: Output completed	Cleared	Cleared	Refreshed at the start or completion of pulse output in independent mode.
	A281.04	Pulse Output 1 Outputting Pulses Flag	ON when pulses are being output from pulse output 1. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
	A281.05	Pulse Output 1 No-origin Flag	ON when the origin has not been determined for pulse output 1 and goes OFF when the origin has been determined. OFF: Origin established, ON: Origin not established	Turned ON when operation starts.	Turned ON when power is turned ON.	<ul style="list-style-type: none"> • Turned ON when the pulse output is reset. • Turned ON when an origin search is started. • Turned ON when a limit input is received and clearing is set. • Turned ON when an overflow or underflow occurs. • Turned OFF when an origin search is completed. • Turned OFF when INI(880) instruction is executed to change the PV.
	A281.06	Pulse Output 1 At-origin Flag	ON when the PV of pulse output 1 matches the origin (0). OFF: Not stopped at origin, ON: Stopped at origin	Retained	Cleared	<ul style="list-style-type: none"> • Turned ON when stopped at the origin. • Turned OFF when the origin is left.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A281	A281.07	Pulse Output 1 Output Stopped Error Flag	ON when an error occurred while outputting pulses in the pulse output 1 origin search function. The Pulse Output 1 Output Stop Error Code will be written to A445. 0: No error, ON: Stop error	Retained	Cleared	<ul style="list-style-type: none"> Cleared when an origin search is started. Refreshed when a fatal pulse output error occurs during an origin search. Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
	A281.08	Pulse Output 1 Interrupt Feeding In-progress Flag	This flag is turned ON when an interrupt input is received after output from pulse output 1 is started with the IFEED(892) instruction. OFF: Interrupt feeding not in progress. ON: Interrupt feeding in progress.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared during overseeing processing after completing interrupt feeding. Turned ON when the interrupt input turns ON after pulse output is started with the IFEED(892) instruction.
	A281.09	Pulse Output 1 Interrupt Feeding Error Flag	This flag will turn ON if an overflow or underflow occurs when an interrupt input is received, or when the specified number of pulses is moved, after output from pulse outputs 1 is started with the IFEED(892) instruction. ON: No error. OFF: Overflow/underflow or specified number of pulses has been moved.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when IFEED(892) instruction processing is started. Turned ON if an overflow or underflow occurs when an interrupt input is received, or if an overflow or underflow occurs while the specified number of pulses is being moved, after operation is started with the IFEED(892) instruction with the origin defined.
A283	A283.00	PWM Output 0 Output In-progress Flag	ON when pulses are being output from PWM output 0. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
	A283.08	PWM Output 1 Output In-progress Flag	ON when pulses are being output from PWM output 1. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
A293	---	Version Error Information	A value is set here when the transferred user program contains a function that is not supported by the unit version of the CPU Unit. 0000 hex: No error. 0001 hex: Error	Cleared	Cleared	Written at the start of operation

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A294	---	Task Number when Program Stopped	<p>This word contains the task number of the task that was being executed when program execution was stopped because of a program error.</p> <p>Normal tasks: 0000 to 007F hex (task 0 to 127)</p> <p>Interrupt tasks: 8000 to 80FF hex (task 0 to 255)</p> <p>A298 and A299 contain the program address where program execution was stopped.</p>	Cleared	Cleared	A298/A299
A295	A295.08	Instruction Processing Error Flag	<p>This flag and the Error Flag (ER) will be turned ON when an instruction processing error has occurred and the PLC Setup has been set to stop operation for an instruction error. CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON.</p> <p>ON: Error Flag ON OFF: Error Flag OFF</p>	Cleared	Cleared	<p>PLC Setup (Operation when instruction error has occurred)</p> <p>The task number where the error is stored in A294 and the program address is stored in A298 and A299.</p>
	A295.09	Indirect DM/EM BCD Error Flag	<p>This flag and the Access Error Flag (AER) will be turned ON when an indirect DM/EM BCD error has occurred and the PLC Setup has been set to stop operation an indirect DM/EM BCD error. (This error occurs when the content of an indirectly addressed DM or EM word is not BCD although BCD mode has been selected.) CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON.</p> <p>ON: Not BCD OFF: Normal</p>			
	A295.10	Illegal Access Error Flag	<p>This flag and the Access Error Flag (AER) will be turned ON when an illegal access error has occurred and the PLC Setup has been set to stop operation an illegal access error. (This error occurs when a region of memory is access illegally.) CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON.</p> <p>The following operations are considered illegal access:</p> <p>Reading/writing the system area Reading/writing EM File Memory Writing to a write-protected area Indirect DM/EM BCD error (in BCD mode)</p> <p>ON: Illegal access occurred OFF: Normal condition</p>			

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A295	A295.11	No END Error Flag	ON when there is not an END(001) instruction in each program within a task. CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON. ON: No END OFF: Normal condition	Cleared	Cleared	The task number where the error is stored in A294 and the program address is stored in A298 and A299.
	A295.12	Task Error Flag	ON when a task error has occurred. The following conditions generate a task error. There is not even one regular task that is executable (started). There is not a program allocated to the task. ON: Error OFF: Normal			
	A295.13	Differentiation Overflow Error Flag	The allowed value for Differentiation Flags which correspond to differentiation instructions has been exceeded. CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON. ON: Error OFF: Normal			
	A295.14	Illegal Instruction Error Flag	ON when a program that cannot be executed has been stored. CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON. ON: Error OFF: Normal			
	A295.15	UM Overflow Error Flag	ON when the last address in UM (User Memory) has been exceeded. CPU Unit operation will stop and the ERR/ALM indicator will light when this flag goes ON. ON: Error OFF: Normal			
A298 and A299	---	Program Address Where Program Stopped	These words contain the 8-digit binary program address of the instruction where program execution was stopped due to a program error.	Cleared	Cleared	(A294 contains the task number of the task where program execution was stopped.)
A300	---	Error Log Pointer	When an error occurs, the Error Log Pointer is incremented by 1 to indicate the location where the next error record will be recorded as an offset from the beginning of the Error Log Area (A100 to A199). 00 to 14 hexadecimal The Error Log Pointer can be cleared to 00 by turning A500.14 (the Error Log Reset Bit) from OFF to ON. When the Error Log Pointer has reached 14 hex (20 decimal), the next record is stored in A195 to A199 when the next error occurs.	Retained	Retained	Refreshed when error occurs. A500.14
A301	---	Current EM Bank---	This word contains the current EM bank number in 4-digit hexadecimal. The current bank number can be changed with the EMBC(281) instruction. 0000 to 0018 hexadecimal	Cleared	Cleared	---
A302	A302.00 to A302.15	CPU Bus Unit Initializing Flags	These flags are ON while the corresponding CPU Bus Unit is initializing after its CPU Bus Unit Restart Bit (A501.00 to A501.15) is turned from OFF to ON or the power is turned ON. Bits 00 to 15 correspond to unit numbers 0 to 15. Use these flags in the program to prevent the CPU Bus Unit's refresh data from being used while the Unit is initializing. IORF(097) and FIORF(225) (CJ2 and CJ1H-R CPU Units only) cannot be executed while an CPU Bus Unit is initializing. OFF: Not initializing ON: Initializing (Reset to 0 automatically after initialization.) These bits are turned OFF automatically when initialization is completed.	Retained	Cleared	Written during initialization A501.00

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A315	A315.12	Tag Memory Error Flag (Non-fatal Error) (CJ2H-CPU6□-EIP only.)	ON when an error occurs in the tag memory where network symbols are stored.	Retained	Cleared	---
	A315.13	Option Board Error Flag	Turns ON when the Option Board is removed while the power is being supplied or an Option Board that is not supported is mounted. OFF: No error, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Written when power is turned ON. Refreshed each cycle during over-seeing process.
	A315.15	Backup Memory Error Flag	ON when writing to the backup data area, source memory area, or comment memory area in the internal flash memory fails. This bit will turn OFF when writing is completed successfully.	Retained	Cleared	---
A316 and A317	---	High-speed Counter 2 PV	Contains the PV of high-speed counter 2. Lower 4 digits: A316, Upper 4 digits: A317	Cleared	Cleared	---
A318 and A319	---	High-speed Counter 3 PV	Contains the PV of high-speed counter 3. Lower 4 digits: A318, Upper 4 digits: A319	Cleared	Cleared	---
A320	A320.00	High-speed Counter 2 Range Comparison Condition 1 In-range Flag	These flags indicate whether the PV is within any of the eight ranges when high-speed counter 2 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left. OFF: Not in range, ON: In range	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Refreshed when reset.
	A320.01	High-speed Counter 2 Range Comparison Condition 2 In-range Flag				
	A320.02	High-speed Counter 2 Range Comparison Condition 3 In-range Flag				
	A320.03	High-speed Counter 2 Range Comparison Condition 4 In-range Flag				
	A320.04	High-speed Counter 2 Range Comparison Condition 5 In-range Flag				
	A320.05	High-speed Counter 2 Range Comparison Condition 6 In-range Flag				
	A320.06	High-speed Counter 2 Range Comparison Condition 7 In-range Flag				
	A320.07	High-speed Counter 2 Range Comparison Condition 8 In-range Flag				
	A320.08	High-speed Counter 2 Comparison In-progress Flag	This flag indicates whether a comparison operation is being executed for high-speed counter 2. OFF: Stopped, ON: Comparing	Cleared	Cleared	Refreshed when comparison operation starts or stops.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A320	A320.09	High-speed Counter 2 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of high-speed counter 2. (Used with the linear mode counting range only.) OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the PV is changed. Refreshed when an overflow or underflow occurs.
	A320.10	High-speed Counter 2 Count Direction	This flag indicates whether high-speed counter 2 is currently being incremented or decremented. The counter PV for the current cycle is compared with the PV in last cycle to determine the direction. OFF: Decrementing, ON: Incrementing	---	---	<ul style="list-style-type: none"> Setting used for high-speed counter, valid during counter operation. Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status.
A321	A321.00	High-speed Counter 3 Range Comparison Condition 1 In-range Flag	These flags indicate whether the PV is within any of the eight ranges when high-speed counter 3 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left. OFF: Not in range, ON: In range	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Refreshed when reset.
	A321.01	High-speed Counter 3 Range Comparison Condition 2 In-range Flag				
	A321.02	High-speed Counter 3 Range Comparison Condition 3 In-range Flag				
	A321.03	High-speed Counter 3 Range Comparison Condition 4 In-range Flag				
	A321.04	High-speed Counter 3 Range Comparison Condition 5 In-range Flag				
	A321.05	High-speed Counter 3 Range Comparison Condition 6 In-range Flag				
	A321.06	High-speed Counter 3 Range Comparison Condition 7 In-range Flag				
	A321.07	High-speed Counter 3 Range Comparison Condition 8 In-range Flag				
	A321.08	High-speed Counter 3 Comparison In-progress Flag	This flag indicates whether a comparison operation is being executed for high-speed counter 3. OFF: Stopped, ON: Comparing	Cleared	Cleared	Refreshed when comparison operation starts or stops.
	A321.09	High-speed Counter 3 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of high-speed counter 3. (Used with the linear mode counting range only.) OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the PV is changed. Refreshed when an overflow or underflow occurs.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A321	A321.10	High-speed Counter 3 Count Direction	This flag indicates whether high-speed counter 3 is currently being incremented or decremented. The counter PV for the current cycle is compared with the PV in last cycle to determine the direction. OFF: Decrementing, ON: Incrementing	---	---	<ul style="list-style-type: none"> Setting used for high-speed counter, valid during counter operation. Refreshed each cycle during over-seeing process. Refreshed when PRV(881) instruction is executed to read the PV or status.
A322 and A323	---	Pulse Output 2 PV	Contain the number of pulses output from the corresponding pulse output port.	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when INI(880) instruction is executed to change the PV of the corresponding counter. Cleared when Pulse Output Reset Bit is turned ON. Cleared when pulse output is started (when the origin is not defined). Refreshed when PRV(881) instruction is executed to read the PV or status.
A324 and A325	---	Pulse Output 3 PV	Contain the number of pulses output from the corresponding pulse output port. 8000 0000 to 7FFF FFFF hex (–2,147,483,648 to 2,147,483,647) When pulses are being output in the CW direction, the PV is incremented by 1 for each pulse. When pulses are being output in the CCW direction, the PV is decremented by 1 for each pulse. PV after overflow: 7FFF FFFF hex PV after underflow: 8000 0000 hex Lower 4 digits: A276/A278/A322/A324 Upper 4 digits: A277/A279/A323/A325	Cleared	Cleared	
A326	A326.00	Pulse Output 2 Pulse Output Status Flag	This flag will be ON when pulses are being output from pulse output 2 according to an ORG(889), ACC(888), PLS2(887), or IFEEED(892) instruction and the output frequency is being changed in steps (accelerating or decelerating). OFF: Constant speed, ON: Accelerating/decelerating	Cleared	Cleared	Refreshed each cycle during over-seeing process.
	A326.01	Pulse Output 2 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of pulse output 2. OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when the INI(880) instruction is executed to change the PV. Refreshed when an overflow or underflow occurs.
	A326.02	Pulse Output 2 Number of Pulses Set Flag	ON when the number of output pulses for pulse output 2 has been set with the PULS(886) instruction. OFF: Not set, ON: Set	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed when the PULS(886) instruction is executed. Cleared when pulse output is stopped.
	A326.03	Pulse Output 2 Output Completed Flag	ON when the number of output pulses set with the PULS(886), PLS2(887), or IFEEED(892) instruction has been output through pulse output 2. OFF: Output not completed, ON: Output completed	Cleared	Cleared	Refreshed at the start or completion of pulse output in independent mode.
	A326.04	Pulse Output 2 Outputting Pulses Flag	ON when pulses are being output from pulse output 2. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A326	A326.05	Pulse Output 2 No-origin Flag	ON when the origin has not been determined for pulse output 2 and goes OFF when the origin has been determined. OFF: Origin established, ON: Origin not established	Turned ON when operation starts.	Turned ON when power is turned ON.	Refreshed each cycle during over-seeing process.
	A326.06	Pulse Output 2 At-origin Flag	ON when the PV of pulse output 2 matches the origin (0). OFF: Not stopped at origin, ON: Stopped at origin	Cleared	Cleared	Refreshed each cycle during over-seeing process.
	A326.07	Pulse Output 2 Output Stopped Error Flag	ON when an error occurred while outputting pulses in the pulse output 2 origin search function. The Pulse Output 2 Output Stop Error Code will be written to A438. 0: No error, ON: Stop error	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
	A326.08	Pulse Output 2 Interrupt Feeding In-progress Flag	These flags are turned ON when an interrupt input is received after output from pulse outputs 2 to 3 is started with the IFEEED(892) instruction. OFF: Interrupt feeding not in progress. ON: Interrupt feeding in progress.	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared during overseeing processing after completing interrupt feeding. • Refreshed when interrupt input is received after starting pulse output with IFEEED(892) instruction.
	A326.09	Pulse Output 2 Interrupt Feeding Error Flag	This flag will turn ON if an overflow or underflow occurs when an interrupt input is received, or when the specified number of pulses is moved, after output from pulse outputs 2 is started with the IFEEED(892) instruction. OFF: Overflow/underflow or specified number of pulses has been moved. ON: No error.	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared when IFEEED(892) instruction processing is started. • Turned ON if an overflow or underflow occurs when an interrupt input is received, or if an overflow or underflow occurs while the specified number of pulses is being moved, after operation is started with the IFEEED(892) instruction with the origin defined.
A327	A327.00	Pulse Output 3 Pulse Output Status Flag	This flag will be ON when pulses are being output from pulse output 3 according to an ORG(889), ACC(888), PLS2(887), or IFEEED(892) instruction and the output frequency is being changed in steps (accelerating or decelerating). OFF: Constant speed, ON: Accelerating/decelerating	Cleared	Cleared	Refreshed each cycle during over-seeing process.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A327	A327.01	Pulse Output 3 Overflow/Underflow Flag	This flag indicates when an overflow or underflow has occurred in the PV of pulse output 3. OFF: Normal, ON: Error	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared when the INI(880) instruction is executed to change the PV. • Refreshed when an overflow or underflow occurs.
	A327.02	Pulse Output 3 Number of Pulses Set Flag	ON when the number of output pulses for pulse output 3 has been set with the PULS(886) instruction. OFF: Not set, ON: Set	Cleared	Cleared	<ul style="list-style-type: none"> • Refreshed when the PULS(886) instruction is executed. • Cleared when pulse output is stopped.
	A327.03	Pulse Output 3 Output Completed Flag	ON when the number of output pulses set with the PULS(886), PLS2(887), or IFEED(892) instruction has been output through pulse output 3. OFF: Output not completed, ON: Output completed	Cleared	Cleared	Refreshed at the start or completion of pulse output in independent mode.
	A327.04	Pulse Output 3 Outputting Pulses Flag	ON when pulses are being output from pulse output 3. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
	A327.05	Pulse Output 3 No-origin Flag	ON when the origin has not been determined for pulse output 3 and goes OFF when the origin has been determined. OFF: Origin established, ON: Origin not established	Turned ON when operation starts.	Turned ON when power is turned ON.	<ul style="list-style-type: none"> • Turned ON when the pulse output is reset. • Turned ON when an origin search is started. • Turned ON when a limit input is received and clearing is set. • Turned ON when an overflow or underflow occurs. • Turned OFF when an origin search is completed. • Turned OFF when INI(880) instruction is executed to change the PV.
	A327.06	Pulse Output 3 At-origin Flag	ON when the PV of pulse output 3 matches the origin (0). OFF: Not stopped at origin, ON: Stopped at origin	Cleared	Cleared	<ul style="list-style-type: none"> • Turned ON when stopped at the origin. • Turned OFF when the origin is left.
	A327.07	Pulse Output 3 Output Stopped Error Flag	ON when an error occurred while outputting pulses in the pulse output 3 origin search function. The Pulse Output 3 Output Stop Error Code will be written to A439. 0: No error, ON: Stop error	Cleared	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A327	A327.08	Pulse Output 3 Interrupt Feeding In-progress Flag	These flags are turned ON when an interrupt input is received after output from pulse outputs 3 to 3 is started with the IFEED(892) instruction. OFF: Interrupt feeding not in progress. ON: Interrupt feeding in progress.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared during overseeing processing after completing interrupt feeding. Refreshed when interrupt input is received after starting pulse output with IFEED(892) instruction.
	A327.09	Pulse Output 3 Interrupt Feeding Error Flag	This flag will turn ON if an overflow or underflow occurs when an interrupt input is received, or when the specified number of pulses is moved, after output from pulse outputs 3 is started with the IFEED(892) instruction. ON: No error. OFF: Overflow/underflow or specified number of pulses has been moved.	Cleared	Cleared	<ul style="list-style-type: none"> Cleared when IFEED(892) instruction processing is started. Turned ON if an overflow or underflow occurs when an interrupt input is received, or if an overflow or underflow occurs while the specified number of pulses is being moved, after operation is started with the IFEED(892) instruction with the origin defined.
A329	A329.00	PWM Output 2 Output In-progress Flag	ON when pulses are being output from PWM output 2. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
	A329.08	PWM Output 3 Output In-progress Flag	ON when pulses are being output from PWM output 3. OFF: Stopped, ON: Outputting	Cleared	Cleared	Refreshed when pulse output starts or stops.
A330 to A335	A330.00 to A335.15	Special I/O Unit Initializing Flags	These flags are ON while the corresponding Special I/O Unit is initializing after its Special I/O Unit Restart Bit (A502.00 to A507.15) is turned from OFF to ON or the power is turned ON. The bits in these words correspond to unit numbers 0 to 95 as follows: A330.00 to A330.15: Units 0 to 15 A331.00 to A331.15: Units 16 to 31 ---- A335.00 to A335.15: Units 80 to 95 Use these flags in the program to prevent the Special I/O Unit's refresh data from being used while the Unit is initializing. Also, IORF(097) and FIORF(225) cannot be executed while a Special I/O Unit is initializing. OFF: Not initializing ON: Initializing (Reset to 0 automatically after initialization.) These bits are turned OFF automatically when initialization is completed.	Retained	Cleared	A502.00 to A507.15
A336	A336.00 to A336.15	Units Detected at Startup (Racks 0 to 3)	The number of Units detected on each Rack is stored in 1-digit hexadecimal (0 to A hex). Rack 0: A336.00 to A336.03 Rack 1: A336.04 to A336.07 Rack 2: A336.08 to A336.11 Rack 3: A336.12 to A336.15 Example: The following would be stored if Rack 0 had 1 Unit, Rack 1 had 4 Units, Rack 2 had 8 Units and Rack 3 had 10 Units: A336 = A 8 4 1	Retained	Cleared	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A339 and A340	---	Maximum Differentiation Flag Number	These words contain the maximum value of the differentiation flag numbers being used by differentiation instructions.	See Function column.	Cleared	Written at the start of operation A295.13
A343	A343.00 to A343.02	Memory Card Type	Indicates the type of Memory Card, if any, installed. 0: None 4: Flash ROM This information is recorded when the PLC power is turned ON or the Memory Card power switch is turned ON.	Retained	See Function column.	See Function column.
	A343.06	EM File Memory Format Error Flag	ON when a format error occurs in the first EM bank allocated for file memory. ON: Format error OFF: No format error The flag is turned OFF when formatting is completed normally.	Retained	Cleared	---
	A343.07	Memory Card Format Error Flag	ON when the Memory Card is not formatted or a formatting error has occurred. (The flag is turned OFF when formatting is completed normally.) ON: Format error OFF: No format error This flag is written when the PLC power is turned ON or the Memory Card power switch is turned ON.	Retained	See Function column.	See Function column.
	A343.08	File Transfer Error Flag	ON when an error occurred while writing data to file memory. ON: Error OFF: No error	Retained	Cleared	Refreshed when file data is written.
	A343.09	File Write Error Flag	ON when data cannot be written to file memory because it is write-protected or the data exceeds the capacity of the file memory. ON: Write not possible OFF: Normal condition	Retained	Cleared	Refreshed when file data is written.
	A343.10	File Read Error	ON when a file could not be read because of a malfunction (file is damaged or data is corrupted). ON: Read not possible OFF: Normal condition or read processing is being executed	Retained	Cleared	Refreshed when file data is read.
	A343.11	File Missing Flag	ON when an attempt is made to read a file that does not exist, or an attempt is made to write to a file in a directory that does not exist. ON: Specified file or directory is missing OFF: Normal condition or read processing is being executed	Retained	Cleared	Refreshed when file data is read.
	A343.13	File Memory Operation Flag	ON while any of the following operations is being executed. OFF when none of them are being executed. CMND instruction sending a FINS command to the local CPU Unit. Execution of a File Memory instruction. Program replacement using the control bit in the Auxiliary Area. Easy backup operation. ON: Instruction being executed. OFF: Instruction not being executed.	Retained	Cleared	Refreshed when file memory instruction is executed.
	A343.14	Accessing File Data Flag	ON while file data is being accessed. ON: File being accessed OFF: File not being accessed Use this flag to prevent two file memory instructions from being executed at the same time.	Retained	Cleared	---
	A343.15	Memory Card Detected Flag	ON when a Memory Card has been detected. OFF when a Memory Card has not been detected. ON: Memory Card detected OFF: Memory Card not detected	Retained	Cleared	Refreshed when Memory Card is inserted, or the power is turned ON.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A344	A344.00 to A344.07	EM File Memory Starting Bank	Contains the starting bank number of EM trace memory or EM file memory. All EM banks from this starting bank to the last bank in the EM Area are formatted for use as file memory or trace memory. If EM Area banks are not formatted to file memory or trace memory, this A344 will be FFFF hex. To convert part of the EM Area to file memory or trace memory, select PLC - Memory Allocation - EM Memory Settings from the CX-Programmer, and then select File Memory or Trace Memory.	Retained	Retained	PLC Setup (EM File Setting Enabled parameter and EM Start File No. parameter)
	A344.14	EM Trace Memory Flag	When A344 is not FFFF hex and this flag is ON, the banks of the EM Area from the bank given in A344.00 to A344.07 to the end of the EM Area are formatted to trace memory.	Retained	Retained	
	A344.15	EM File Memory Flag	When A344 is not FFFF hex and this flag is ON, the banks of the EM Area from the bank given in A344.00 to A344.07 to the end of the EM Area are formatted to file memory.	Retained	Retained	
A345	A345.00	FB Program Source Information Flag	Turns ON when there is FB program source information in the source/comment memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	When transferred from the CX-Programmer
	A345.01	Symbol Table Information Flag	Turns ON when there is symbol table information in the source/comment memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	
	A345.02	Comment Information Flag	Turns ON when there is comment information in the source/comment memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	
	A345.03	Program Index Information Flag	Turns ON when there is program index information in the source/comment memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	
	A345.05	SFC Program Source Information Flag	Turns ON when there is SFC program source information in the source/comment memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	
	A345.07	Network Symbols (Tags) Information Flag	Turns ON when there is network symbols (tags) information in the tag memory. ON: Information present, OFF: Information not present	Retained	Internal status updated	
A346 and A347	---	Number of Remaining Words to Transfer	These words contain the 8-digit hexadecimal number of words remaining to be transferred by FREAD(700) or FWRT(701). When one of these instructions is executed, the number of words to be transferred is written to A346 and A347. While the data is being transferred, the value in these words is decremented. A346 contains the rightmost 4-digits and A347 contains the leftmost 4-digits. Check the content of these words to determine whether or not the planned number of words have been transferred successfully.	Retained	Cleared	Written as FREAD or FWRT is being executed. Decrement as data is actually transferred.
A351 to A354	---	Calendar/Clock Area	These words contain the CPU Unit's internal clock data in BCD. The clock can be set from the CX-Programmer, with the DATE(735) instruction, or with a FINS command (CLOCK WRITE, 0702). A351.00 to A351.07: Seconds (00 to 59) (BCD) A351.08 to A351.15: Minutes (00 to 59) (BCD) A352.00 to A352.07: Hours (00 to 23) (BCD) A352.08 to A352.15: Day of the month (01 to 31) (BCD) A353.00 to A353.07: Month (01 to 12) (BCD) A353.08 to A353.15: Year (00 to 99) (BCD) A354.00 to A354.07: Day of the week (00 to 06) (BCD) 00: Sunday, 01: Monday, 02: Tuesday, 03: Wednesday, 04: Thursday, 05: Friday, 06: Saturday	Retained	Retained	Written every cycle

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A360 to A391	A360.01 to A391.15	Executed FAL Number Flags	The flag corresponding to the specified FAL number will be turned ON when FAL(006) is executed. Bits A360.01 to A391.15 correspond to FAL numbers 001 to 511. ON: That FAL was executed OFF: That FAL wasn't executed The flag will be turned OFF when the error is cleared.	Retained	Cleared	Refreshed when error occurs. A402.15
A392	A392.04	Serial Port Error Flag	ON when an error has occurred at the RS-232C port. (Do not access this bit in Peripheral Bus Mode, 1:N NT Link mode, or Serial PLC Link Polling/Polled Unit mode.) ON: Error OFF: No error	Retained	Cleared	Refreshed when error occurs.
	A392.05	Serial Port Send Ready Flag (No-protocol mode)	ON when the RS-232C port is able to send data in no-protocol mode. ON: Able-to-send OFF: Unable-to-send	Retained	Cleared	Written after transmission
	A392.06	Serial Port Reception Completed Flag (No-protocol mode)	ON when the RS-232C port has completed the reception in no-protocol mode. • When the number of bytes was specified: ON when the specified number of bytes is received. • When the end code was specified: ON when the end code is received or 256 bytes are received.	Retained	Cleared	Written after reception
	A392.07	Serial Port Reception Overflow Flag (No-protocol mode)	ON when a data overflow occurred during reception through the RS-232C port in no-protocol mode. • When the number of bytes was specified: ON when more data is received after the reception was completed but before RXD(235) was executed. • When the end code was specified: ON when more data is received after the end code was received but before RXD(235) was executed. ON when 257 bytes are received before the end code. ON: Overflow OFF: No overflow	Retained	Cleared	
A393	A393.00 to A393.07	Serial Port PT Communications Flag	The corresponding bit will be ON when the serial port is communicating in NT Link Mode or in Serial PLC Link Mode. Bits 0 to 7 correspond to units 0 to 7. ON: Communicating OFF: Not communicating	Retained	Cleared	Refreshed when there is a normal response to the token.
	A393.08 to A393.15	Serial Port PT Priority Registered Flags	The corresponding bit will be ON for the PT that has priority when the RS-232C port is communicating in NT link mode. Bits 0 to 7 correspond to units 0 to 7. These flags are written when the priority registration command is received. ON: Priority registered OFF: Priority not registered	Retained	Cleared	See Function column.
	A393.00 to A393.15	Serial Port Reception Counter (No-protocol mode)	Indicates (in binary) the number of bytes of data received when the RS-232C port is in no-protocol mode.	Retained	Cleared	Refreshed when data is received.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A395	A395.06	File Deleted Flags	The system deleted the remainder of a Memory Card file that was being updated when a power interruption occurred. ON: File deleted OFF: No files deleted	Cleared	Cleared	Refreshed when the system deletes the file.
	A395.07		The system deleted the remainder of an EM file memory file that was being updated when a power interruption occurred. ON: File deleted OFF: No files deleted	Cleared	Cleared	Refreshed when the system deletes the file.
	A395.10	ER/AER Flag for Background Execution	ON when an instruction processing error or an illegal area access error occurs during background processing. ON: Error. OFF (0) when power is turned ON. OFF (0) when operation starts. OFF: No errors. OFF (0) when background processing starts.	Cleared	Cleared	---
	A395.11	Memory Corruption Detected Flag	ON when memory corruption is detected when the power supply is turned ON. ON: Memory corruption OFF: Normal operation	Retained	See Function column.	Refreshed when power is turned ON.
	A395.12	DIP Switch Pin 6 Status Flag	The status of pin 6 on the DIP switch on the front of the CPU Unit is written to this flag every cycle. ON: Pin 6 ON OFF: Pin 6 OFF	Retained	See Function column.	Written every cycle.
A400	---	Error code	When a non-fatal error (user-defined FALS(006) or system error) or a fatal error (user-defined FALS(007) or system error) occurs, the 4-digit hexadecimal error code is written to this word. (Refer to A-3-3 <i>Details on Auxiliary Area Operation</i>) on page A-162. When two or more errors occur simultaneously, the highest error code will be recorded.	Cleared	Cleared	Refreshed when error occurs.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A401	A401.03	Card Transfer Error Flag	<ul style="list-style-type: none"> ON when the contents of the Memory Card were not correctly read into the CPU Unit when the power was turned ON. ON when a program file (.OBJ) that includes network symbols is transferred when the power is turned ON (CJ2H-CPU6□ and CJ2M-CPU1□ only.) 	Cleared for error clear operation.	Cleared	---
	A401.05	Version Error Flag	ON when the unit version of the CPU Unit might not support the user program that was transferred.	Cleared for error clear operation.	Cleared	---
	A401.06	FALS Error Flag (Fatal error)	<p>ON when a non-fatal error is generated by the FALS(006) instruction. The CPU Unit will continue operating and the ERR/ALM indicator will flash.</p> <p>The corresponding error code will be written to A400. Error codes C101 to C2FF correspond to FALS numbers 001 to 511.</p> <p>ON: FALS(006) executed OFF: FALS(006) not executed</p> <p>This flag will be turned OFF when the FALS errors are cleared.</p>	Cleared	Cleared	Refreshed when error occurs. A400
	A401.08	Cycle Time Exceeded Flag (Fatal error)	<p>ON if the cycle time exceeds the maximum cycle time set in the PLC Setup (the cycle time monitoring time). CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>OFF: Cycle time under max. ON: Cycle time over max.</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	Refreshed when the cycle time exceeds maximum. PLC Setup (Cycle time monitoring time)
	A401.09	Program Error Flag (Fatal error)	<p>ON when program contents are incorrect.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light. The task number where the error occurred will be stored in A294 and the program address will be stored in A298 and A299.</p> <p>The type of program error that occurred will be stored in bits 8 to 15 of A295. Refer to the description of A295 and to the <i>Programming Manual</i> for more details on program errors.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A294, A295, A298 and A299
	A401.10	I/O Setting Error Flag (Fatal error)	<ul style="list-style-type: none"> ON when the registered I/O tables do not match actual I/O tables (i.e., when the registered Units do not match the Units that are actually connected). With the CJ2H-CPU6□-EIP, ON when an Interrupt Input Unit is mounted in a slot other than those shown below. CJ2H-CPU6□-EIP: CPU Rack slots 0 to 3 CJ2H-CPU6□ or CJ2M-CPU□□: CPU Rack slots 0 to 4 ON when the registered I/O tables for a CJ2H-CPU6□-EIP or CJ2M-CPU1□ CPU Unit are downloaded to a CJ2H-CPU6□ or CJ2M-CPU1□ CPU Unit, or when the registered I/O tables for a CJ2H-CPU6□ CPU Unit are downloaded to a CJ2H-CPU6□-EIP or CJ2M-CPU3□ CPU Unit. CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light. ON: Error OFF: No error <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A405.08
	A401.11	Too Many I/O Points Flag (Fatal error)	<p>ON when the number of I/O points being used in Basic I/O Units exceeds the maximum allowed for the PLC or when there are more than 11 Units connected in one Rack.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A407

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A401	A401.13	Duplication Error Flag (Fatal error)	<p>ON in the following cases:</p> <ul style="list-style-type: none"> Two CPU Bus Units have been assigned the same unit number. Two Special I/O Units have been assigned the same unit number. Two Basic I/O Units have been allocated the same data area words. <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>The duplicated unit number is indicated in A409 to A416.</p> <p>ON: Duplication error OFF: No duplication</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A410 to A416
	A401.14	I/O Bus Error Flag (Fatal error)	<p>ON when an error occurs in a data transfer between the CPU Unit and a Unit mounted to a slot or when the End Cover is not connected to the CPU Rack or an Expansion Rack.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared.</p> <p>The slot number (00 to 09) where the I/O bus error occurred is written to A404.00 to A404.07 in binary. If an End Cover is not connected, 0E hex will be stored.</p> <p>The rack number (00 to 07) where the I/O bus error occurred is written to A404.08 to A404.15 in binary. These bits will contain 0B hex if an I/O bus error occurs on a CJ2HCPU6□-EIP or CJ2M-CPU3□ built-in network. If an End Cover is not connected, 0E hex will be stored.</p>	Cleared	Cleared	A404
	A401.15	Memory Error Flag (Fatal error)	<p>ON when an error occurred in memory or there was an error in automatic transfer from the Memory Card when the power was turned ON.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>ON: Error OFF: No error</p> <p>The location where the error occurred is given in A403.00 to A403.08.</p> <p>The flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A403

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A402	A402.00	Other Non-fatal Error Flag (System Work Memory Error)	<ul style="list-style-type: none"> ON when a non-fatal error other than a non-fatal error allocated to A402.01 to A402.15 occurs. (E.g., When an error occurs in memory for online editing.) Details of the other non-fatal errors are stored in A315. 	Cleared for error clear operation.	Cleared	---
	A402.02	Special I/O Unit Setting Error Flag (Non-fatal error)	<p>ON when an installed Special I/O Unit does not match the Special I/O Unit registered in the I/O table. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared.</p> <p>The unit number of the Unit where the setting error occurred is indicated in A428 to A433.</p> <p>ON when any of the following occur for a Unit registered in the synchronous unit operation settings.</p> <ul style="list-style-type: none"> The Unit does not support the synchronous unit operation function. The Unit is not connected in the PLC. The Unit is not in the CPU Rack (i.e., it is in an Expansion Rack). <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared and the correct PLC Setup is transferred to the CPU Unit.</p> <p>The unit number of the Unit where the setting error occurred is indicated in A428 to A433.</p>	Cleared	Cleared	A428 to A433
	A402.03	CPU Bus Unit Setting Error Flag (Non-fatal error)	<p>ON when an installed CPU Bus Unit does not match the CPU Bus Unit registered in the I/O table. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p> <p>This flag will be turned OFF when the error is cleared.</p> <p>The unit number of the Unit where the setting error occurred is written to A427</p>	Cleared	Cleared	A427

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A402	A402.04	Battery Error Flag (Non-fatal error)	ON if the CPU Unit's battery is disconnected or its voltage is low and the Detect Battery Error setting has been set in the PLC Setup. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash. ON: Error OFF: No error This flag will be turned OFF when the error is cleared. This flag can be used to control an external warning light or other indicator to indicate that the battery needs to be replaced.	Cleared	Cleared	PLC Setup (Detect Battery Error)
	A402.06	Special I/O Unit Error Flag (Non-fatal error)	ON when an error occurs in a data exchange between the CPU Unit and a Special I/O Unit (including an error in the Special I/O Unit itself). The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash. The Special I/O Unit where the error occurred will stop operating. ON: Error OFF: No error This flag will be turned OFF when the error is cleared. The unit number of the Unit where the data exchange error occurred is indicated in A418 through A423.	Cleared	Cleared	A418 to A423
	A402.07	CPU Bus Unit Error Flag (Non-fatal error)	ON when an error occurs in a data exchange between the CPU Unit and an CPU Bus Unit (including an error in the CPU Bus Unit itself). The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash. The CPU Bus Unit where the error occurred will stop operating. ON: Error OFF: No error This flag will be turned OFF when the error is cleared. The unit number of the Unit where the data exchange error occurred is indicated in A417.	Cleared	Cleared	A417
	A402.10	PLC Setup Error Flag (Non-fatal error)	ON when there is a setting error in the PLC Setup. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash. The location of the error will be written to A406. ON: Error OFF: No error This flag will be turned OFF when the error is cleared. It will also turn OFF for a setting error for synchronous unit operation settings if correct settings are transferred to the Synchronous Unit.	Cleared	Cleared	A406
	A402.12	Basic I/O Unit Error Flag (Non-fatal error)	ON when an error has occurred in a Basic I/O Unit. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash. ON: Error OFF: No error This flag will be turned OFF when the error is cleared. The location of the error will be written to A408.	Cleared	Cleared	A408
	A402.13	Duplicate Refresh Error Flag (Non-fatal error)	ON when the Detect Duplicate Refresh Errors parameter in the PLC Setup is set to detect duplicate refresh errors and one of the following occurs for the same Special I/O Unit. <ul style="list-style-type: none"> • FIORF(225), IORF(097), IORD(222) or IOWR(223) in a cyclic task is competing with FIORF(225), IORF(097), IORD(222) or IOWR(223) in an interrupt task. • FIORF(225), IORF(097), IORD(222) or IOWR(223) was executed in an interrupt task when I/O was being refreshed. If cyclic refreshing is not disabled in the PLC Setup for a Special I/O Unit and FIORF(225), IORF(097), IORD(222) or IOWR(223) is executed for the same Special I/O Unit in an interrupt task, a duplicate refresh error will occur.	Cleared	Cleared	A426, PLC Setup (Detect Duplicate Refresh Errors parameter in the PLC Setup is set to "Detect")

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A402	A402.15	FAL Error Flag (Non-fatal error)	<p>ON when a non-fatal error is generated by executing FAL(006). The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>The bit in A360 to A391 that corresponds to the FAL number specified in FALS(006) will be turned ON and the corresponding error code will be written to A400. Error codes 4101 to 42FF correspond to FAL numbers 001 to 2FF (0 to 511).</p> <p>ON: FALS(006) error occurred OFF: FALS(006) not executed</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A360 to A391, A400
A403	A403.00 to A403.08	Memory Error Location	<p>When a memory error occurs, the Memory Error Flag (A401.15) is turned ON and one of the following flags is turned ON to indicate the memory area where the error occurred.</p> <p>A403.00: User program A403.04: PLC Setup A403.05: Registered I/O Table A403.07: Routing Table A403.08: CPU Bus Unit Settings</p> <p>When a memory error occurs, the CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p> <p>The corresponding flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A401.15
A404	A404.00 to A404.07	I/O Bus Error Slot Number	<p>Contains the 8-bit binary slot number (00 to 09) where an I/O Bus Error occurred. If an I/O bus error occurs in the CJ2H-CPU6□-EIP built-in EtherNet/IP section, 0B hex will be stored. When the End Cover is not connected to the CPU Rack or an Expansion Rack, F hex will be stored.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>00 to 09 (slot number 00 to 09)</p> <p>The I/O Bus Error Flag (A401.14) will be ON.</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A401.14
	A404.08 to A404.15	I/O Bus Error Rack Number	<p>Contains the 8-bit binary rack number (00 to 03) where an I/O Bus Error occurred. If an I/O bus error occurs in the CJ2H-CPU6□-EIP built-in EtherNet/IP section, 0B hex will be stored. When the End Cover is not connected to the CPU Rack or an Expansion Rack, F hex will be stored.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>00 to 03 (rack number 00 to 03)</p> <p>The I/O Bus Error Flag (A401.14) will be ON.</p> <p>This flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A401.14
A405	A405.08	Interrupt Input Unit Position Error Flag	<p>ON when the Interrupt Input Unit is not connected in the following range. If this flag turns ON, an I/O setting error will occur (fatal error), and operation will stop.</p> <ul style="list-style-type: none"> CJ2H-CPU6□-EIP: CPU Rack slots 0 to 3 (the 4 Unit on the right of the CPU Unit) CJ2H-CPU6□: CPU Rack slots 0 to 4 (the 5 Units on the right of the CPU Unit) <p>An error will also occur if the Unit is physically mounted in the range given above but it is not allocated in this range in the I/O tables with a dummy unit registration.</p>	Cleared	Cleared	A401.10

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A407	A407.00 to A407.12	Too Many I/O Points, Details 1	<p>When any of the following values overflows or an duplication error occurs, the corresponding value will be stored as binary data. The operation of the CPU Unit will stop. The ERR/ALM/ indicator on the front of the CPU Unit will light. For information on the reason the error occurred, refer to Too Many I/O Points, Details 2 (A407.13 to A407.15).</p> <ol style="list-style-type: none"> The number of I/O points will be written here when the total number of I/O points set in the I/O Table (excluding Slave Racks) exceed the maximum allowed for the CPU Unit. The number of interrupt input points when the number of interrupt input points exceeds 32. The number of Racks will be written here when the number of Expansion Racks exceeds the maximum. <p>The relevant value will be written here (A407.00 to A407.12) when the error occurs. These bits will be cleared when the error is cleared.</p>	Cleared	Cleared	A401.11, A407.13 to A407.15
	A407.13 to A407.15	Too Many I/O Points, Details 2	<p>The 3-digit binary value of these bits indicates the cause of the Too Many I/O Points Error and shows the meaning of the value written to bits A407.00 to A407.12.</p> <p>Values of 000 to 101 (0 to 5) correspond to causes 1 through 6 described in "Too Many I/O Points, Cause 1," above.</p> <p>000: Too many I/O total 001: Too many interrupt input points 100: Too many Pulse I/O Modules 101: Too many Racks 111: Too many Units on a Rack</p> <p>These bits will be cleared when the error is cleared.</p>	Cleared	Cleared	---
A408	A408.00 to A408.07	Basic I/O Unit Error, Slot Number	<p>When an error has occurred in a Basic I/O Unit, A402.12 will be turned ON and the slot number where the error occurred will be written here in binary.</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>00 to 09 hexadecimal (Slots 0 to 9)</p> <p>These bits will be cleared when the error is cleared.</p>	Cleared	Cleared	A402.12
	A408.08 to A408.15	Basic I/O Unit Error, Rack Number	<p>When an error has occurred in a Basic I/O Unit, A402.12 will be turned ON and the Rack number where the error occurred will be written here in binary.</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>00 to 03 hexadecimal (Racks 0 to 3)</p> <p>These bits will be cleared when the error is cleared.</p>	Cleared	Cleared	A402.12
A409	A409.00 to A409.03	Expansion Rack Number Duplication Flags	<p>The corresponding flag will be turned ON when an Expansion Rack's starting word address was set from the CX-Programmer and two Racks have overlapping word allocations or a Rack's starting address exceeds CIO 0901. Bits 00 to 03 correspond to Racks 0 to 3.</p> <p>ON: Same words allocated to two different Racks or Rack starting address exceeds CIO 0901. OFF: No error</p> <p>The corresponding flag will be cleared when the error is cleared.</p>	Cleared	Cleared	---
A410	A410.00 to A410.15	CPU Bus Unit Number Duplication Flags	<p>The Duplication Error Flag (A401.13) and the corresponding flag in A410 will be turned ON when an CPU Bus Unit's unit number has been duplicated. Bits 00 to 15 correspond to unit numbers 0 to F.</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>ON: Duplication detected OFF: No duplication</p>	Cleared	Cleared	A401.13

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A411 to A416	A411.00 to A416.15	Special I/O Unit Number Duplication Flags	<p>The Duplication Error Flag (A401.13) and the corresponding flag in A411 through A416 will be turned ON when a Special I/O Unit's unit number has been duplicated.</p> <p>Bits 00 to 15 correspond to unit numbers 0 to F.</p> <p>(Bits A411.00 to A416.15 correspond to unit numbers 000 to 05F (0 to 95).)</p> <p>CPU Unit operation will stop and the ERR/ALM indicator on the front of the CPU Unit will light.</p> <p>The corresponding bit will also be turned ON when the Special I/O Unit's words are also allocated to a Basic I/O Unit on an Expansion Rack because of the Expansion Rack's starting word setting.</p> <p>ON: Duplication detected OFF: No duplication</p>	Cleared	Cleared	A401.13
A417	A417.00 to A417.15	CPU Bus Unit Error, Unit Number Flags	<p>When an error occurs in a data exchange between the CPU Unit and an CPU Bus Unit, the CPU Bus Unit Error Flag (A402.07) is turned ON and the bit in A417 corresponding to the unit number of the Unit where the error occurred is turned ON. Bits 00 to 15 correspond to unit numbers 0 to F.</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p>	Cleared	Cleared	A402.07
A418 to A423	A418.00 to A423.15	Special I/O Unit Error, Unit Number Flags	<p>When an error occurs in a data exchange between the CPU Unit and a Special I/O Unit, the Special I/O Unit Error Flag (A402.06) will be turned ON.</p> <p>Each bit corresponds to a unit number. Bit 00 in A418 to bit 15 in A423 correspond to unit numbers 0 to 95.</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Error OFF: No error</p> <p>The flag will be turned OFF when the error is cleared.</p> <p>If the unit number of the Unit is uncertain, none of the flags will be turned ON.</p>	Cleared	Cleared	A402.06
A426	A426.00 to A426.11	Duplicate Refresh Error Unit Number	<p>When A426.13 is ON, these bits: Contain the unit number of the Special I/O Unit for which duplicate refreshing was performed.</p> <p>These bits will be cleared when the error is cleared.</p> <p>Unit number: 000 to 05F (0 to 95)</p> <p>The flag will be turned OFF when the error is cleared.</p>	Cleared	Cleared	A402.13 A426.15
	A426.15	Duplicate Refresh Error Cause	<p>When A402.13 (the Duplicate Refresh Error Flag) is ON, this flag indicates the cause of the error. The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Duplicated refreshing</p>	Cleared	Cleared	A402.13, A426.00 to A426.11
A427	A427.00 to A427.15	CPU Bus Unit Setting Error, Unit Number Flags	<p>When an CPU Bus Unit Setting Error occurs, A402.03 and the bit in this word corresponding to the Unit's unit number are turned ON. Bits 00 to 15 correspond to unit numbers 0 to F.</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p> <p>ON: Setting error OFF: No setting error</p>	Cleared	Cleared	Refreshed when power is turned ON or I/O is recognized. A402.03
A428 to A433	A428.00 to A433.15	Special I/O Unit Setting Error, Unit Number Flags	<p>When a Special I/O Unit Setting Error occurs, A402.02 and the bit in these words corresponding to the Unit's unit number are turned ON. Bits 00 to 15 correspond to unit numbers 0 to F.</p> <p>ON: Setting error OFF: No setting error</p> <p>The CPU Unit will continue operating and the ERR/ALM indicator on the front of the CPU Unit will flash.</p>	Cleared	Cleared	Refreshed when power is turned ON or I/O is recognized. A402.02

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A438	---	Pulse Output 2 Stop Error Code	If a Pulse Output Stop Error occurs for pulse output 2, the error code is written to this word.	Retained	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
A439	---	Pulse Output 3 Stop Error Code	If a Pulse Output Stop Error occurs for pulse output 3, the error code is written to this word.	Retained	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
A440	---	Max. Interrupt Task Processing Time	Contains the Maximum Interrupt Task Processing Time in units of 0.1 ms. (This value is written after the interrupt task with the max. processing time is executed and cleared when PLC operation begins.) Disabled when high-speed interrupt function is enabled in the PLC Setup.	Cleared	Cleared	See Function column.
A441	---	Interrupt Task With Max. Processing Time	Contains the task number of the interrupt task with the maximum processing time. Hexadecimal values 8000 to 80FF correspond to task numbers 00 to FF. Bit 15 is turned ON when an interrupt has occurred. (This value is written after the interrupt task with the max. processing time is executed and cleared when PLC operation begins.) Disabled when high-speed interrupt function is enabled in the PLC Setup.	Cleared	Cleared	See Function column.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A444	---	Pulse Output 0 Stop Error Code	If a Pulse Output Stop Error occurs for pulse output 0, the error code is written to this word.	Retained	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
A445	---	Pulse Output 1 Stop Error Code	If a Pulse Output Stop Error occurs for pulse output 1, the error code is written to this word.	Retained	Cleared	<ul style="list-style-type: none"> • Cleared when an origin search is started. • Refreshed when a fatal pulse output error occurs during an origin search. • Refreshed when the limit input signal for pulse output is set to be always enabled in the PLC Setup and pulse output is stopped due to the limit input. • Cleared when both limit inputs are disabled and a fatal pulse output error code is stored.
A446	A446.00 to A446.07	Number of Times Protection Has Been Disabled	<ul style="list-style-type: none"> • Counts up each time protection disable fails (i.e., due to the protection disable password being input incorrectly). • Displays the total number of times that protection was disabled for UM protection and task protection. • The counter stops counting when it reaches 255 (decimal). • When all protection has been disabled, the counter will be set to 00 hex. 	---	---	---
A450	---	CIO Area Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A451	---	Word Area Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A452	---	Holding Area Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A459	---	Index Register Area Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A460	---	DM Area Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A461	---	EM Bank 0 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A462	---	EM Bank 1 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A463	---	EM Bank 2 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A464	---	EM Bank 3 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A465	---	EM Bank 4 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A466	---	EM Bank 5 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A467	---	EM Bank 6 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A468	---	EM Bank 7 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A469	---	EM Bank 8 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A470	---	EM Bank 9 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A471	---	EM Bank A Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A472	---	EM Bank B Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A473	---	EM Bank C Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A474	---	EM Bank D Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A475	---	EM Bank E Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A476	---	EM Bank F Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A477	---	EM Bank 10 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A478	---	EM Bank 11 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A479	---	EM Bank 12 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A480	---	EM Bank 13 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A481	---	EM Bank 14 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A482	---	EM Bank 15 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A483	---	EM Bank 16 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A484	---	EM Bank 17 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A485	---	EM Bank 18 Designation	This word stores a fixed code that is used to specify the area when an address is specified as a parameter for a function block instead of an input-output variable (VER_IN_OUT). This code is used only by the OMRON FB Library. Do not change the contents of this word.	Fixed value is set.	Fixed value is set.	---
A10001 to A10003	---	Manufacturing Lot Number	The manufacturing lot number of the CPU Unit is stored as BCD data. Hardware can be identified by referring to this area. hex A10000.00 to A10000.07: 00 hex A10000.08 to A10000.15: 00 hex A10001.00 to A10000.07: 00 hex A10001.08 to A10001.15: Day (01 to 31) A10002.00 to A10002.07: Month (01 to 12) A10002.08 to A10002.15: Year (00 to 09) A10003.00 to A10003.07: 00 hex A10003.08 to A10003.15: 00 hex	---	---	---
A10100	A10100.00	Synchronous Unit Operation Servicing Flag	ON while synchronous unit operation is being performed. ON: Synchronous unit operation being performed OFF: Synchronous unit operation not being performed	Cleared	Cleared	Refreshed when the synchronous signal turns ON the second time after power is turned ON or Unit is restarted.
A10101	---	Synchronous Input Data Refresh Error Code	This word contains 0001 hex when the CPU Unit fails to receive synchronous input data from a Synchronous Unit within the specified time. 0001 hex: Error 0000 hex: Normal	Cleared	Cleared	When synchronous data is refreshed.
A10102	---	Synchronous Operation Cycle Time	This word contains the synchronous operation cycle time set in the PLC Setup.	Retained	Retained	When power is turned ON or the Unit is restarted.
A10120 and A10121	---	Pulse Output 0 Frequency	Contains the frequency of pulse output 0 when tracing pulse output 0 with data tracing. Valid only when the data tracing parameters are set.	---	Cleared	---
A10122 and A10123	---	Pulse Output 1 Frequency	Contains the frequency of pulse output 1 when tracing pulse output 1 with data tracing. Valid only when the data tracing parameters are set.	---	Cleared	---
A10124 and A10125	---	Pulse Output 2 Frequency	Contains the frequency of pulse output 2 when tracing pulse output 2 with data tracing. Valid only when the data tracing parameters are set.	---	Cleared	---
A10126 and A10127	---	Pulse Output 3 Frequency	Contains the frequency of pulse output 3 when tracing pulse output 3 with data tracing. Valid only when the data tracing parameters are set.	---	Cleared	---
A10128 and A10129	---	High-speed Counter 0 Range Comparison Condition 1 to 32 In-range Flags	These flags indicate whether the PV is within any of the one to 32 ranges when high-speed counter 0 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left. OFF: Not in range, ON: In range Bits 00 to 15 in the lower word correspond to ranges 1 to 16. Bits 00 to 15 in the upper word correspond to ranges 17 to 32.	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when comparison is executed for 1 to 32 ranges. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Reset

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A10130 and A10131	---	High-speed Counter 1 Range Comparison Condition 1 to 32 In-range Flags	<p>These flags indicate whether the PV is within any of the one to 32 ranges when high-speed counter 1 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left.</p> <p>OFF: Not in range, ON: In range</p> <p>Bits 00 to 15 in the lower word correspond to ranges 1 to 16. Bits 00 to 15 in the upper word correspond to ranges 17 to 32.</p>	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when comparison is executed for 1 to 32 ranges. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Reset
A10132 and A10133	---	High-speed Counter 2 Range Comparison Condition 1 to 32 In-range Flags	<p>These flags indicate whether the PV is within any of the one to 32 ranges when high-speed counter 2 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left.</p> <p>OFF: Not in range, ON: In range</p> <p>Bits 00 to 15 in the lower word correspond to ranges 1 to 16. Bits 00 to 15 in the upper word correspond to ranges 17 to 32.</p>	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when comparison is executed for 1 to 32 ranges. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Reset
A10134 and A10135	---	High-speed Counter 3 Range Comparison Condition 1 to 32 In-range Flags	<p>These flags indicate whether the PV is within any of the one to 32 ranges when high-speed counter 3 is being operated in range-comparison mode with upper and lower limits. The In-range Flags, however, will be ON whenever the comparison value is within the range regardless of the whether the high-speed counter is set to execute the interrupt task when the range is entered or left.</p> <p>OFF: Not in range, ON: In range</p> <p>Bits 00 to 15 in the lower word correspond to ranges 1 to 16. Bits 00 to 15 in the upper word correspond to ranges 17 to 32.</p>	Cleared	Cleared	<ul style="list-style-type: none"> Refreshed each cycle during over-seeing process. Refreshed when comparison is executed for 1 to 32 ranges. Refreshed when PRV(881) instruction is executed to read the results of range comparison. Refreshed when INI(880) instruction is executed to change PV or ring counter maximum value. Reset
A10136 and A10137	---	High-speed Counter 0 Ring Counter Maximum Value	<p>Contain the ring counter maximum values when high-speed counters 0 is used as ring counters. These values are cleared to 0 if Linear Mode is used.</p> <p>Lower 4 digits: A10136, Upper 4 digits: A10137</p>	Cleared	Cleared	Refreshed when INI(880) instruction is executed to change ring counter maximum value.
A10138 and A10139	---	High-speed Counter 1 Ring Counter Maximum Value	<p>Contain the ring counter maximum values when high-speed counters 1 is used as ring counters. These values are cleared to 0 if Linear Mode is used.</p> <p>Lower 4 digits: A10138, Upper 4 digits: A10139</p>	Cleared	Cleared	Refreshed when INI(880) instruction is executed to change ring counter maximum value.

Address		Name	Function	Status after mode change	Status at startup	Write timing/ Related flags, settings
Words	Bits					
A10140 and A10141	---	High-speed Counter 2 Ring Counter Maximum Value	Contain the ring counter maximum values when high-speed counters 2 is used as ring counters. These values are cleared to 0 if Linear Mode is used. Lower 4 digits: A10140, Upper 4 digits: A10141	Cleared	Cleared	Refreshed when INI(880) instruction is executed to change ring counter maximum value.
A10142 and A10143	---	High-speed Counter 3 Ring Counter Maximum Value	Contain the ring counter maximum values when high-speed counters 3 is used as ring counters. These values are cleared to 0 if Linear Mode is used. Lower 4 digits: A10142, Upper 4 digits: A10143	Cleared	Cleared	Refreshed when INI(880) instruction is executed to change ring counter maximum value.
A10144 and A10145	---	Interrupt input 0 latched PV	When there is an input for interrupt input 0, the PV of pulse output 0 or the PV of high-speed counter input 0 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10144, Upper 4 digits: A10145	Cleared	Cleared	When input interrupt occurs
A10146 and A10147	---	Interrupt input 1 latched PV	When there is an input for interrupt input 1, the PV of pulse output 1 or the PV of high-speed counter input 1 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10146, Upper 4 digits: A10147	Cleared	Cleared	When input interrupt occurs
A10148 and A10149	---	Interrupt input 2 latched PV	When there is an input for interrupt input 2, the PV of pulse output 2 or the PV of high-speed counter input 2 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10148, Upper 4 digits: A10149	Cleared	Cleared	When input interrupt occurs
A10150 and A10151	---	Interrupt input 3 latched PV	When there is an input for interrupt input 3, the PV of pulse output 3 or the PV of high-speed counter input 3 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10150, Upper 4 digits: A10151	Cleared	Cleared	When input interrupt occurs
A10152 and A10153	---	Interrupt input 4 latched PV	When there is an input for interrupt input 4, the PV of pulse output 4 or the PV of high-speed counter input 4 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10152, Upper 4 digits: A10153	Cleared	Cleared	When input interrupt occurs
A10154 and A10155	---	Interrupt input 5 latched PV	When there is an input for interrupt input 5, the PV of pulse output 5 or the PV of high-speed counter input 5 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10154, Upper 4 digits: A10155	Cleared	Cleared	When input interrupt occurs
A10156 and A10157	---	Interrupt input 6 latched PV	When there is an input for interrupt input 6, the PV of pulse output 6 or the PV of high-speed counter input 6 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10156, Upper 4 digits: A10157	Cleared	Cleared	When input interrupt occurs
A10158 and A10159	---	Interrupt input 7 latched PV	When there is an input for interrupt input 7, the PV of pulse output 7 or the PV of high-speed counter input 7 is stored. The PV immediately before the interrupt input task is started is read and saved. Lower 4 digits: A10158, Upper 4 digits: A10159	Cleared	Cleared	When input interrupt occurs

Note In CJ-series PLCs, the following flags are provided in a special read-only area and can be specified with the labels given in the table. These flags are not contained in the Auxiliary Area. Refer to 6-21 *Condition Flags* and 6-22 *Clock Pulses* for details.